

ASSISTANT COMMISSIONER FOR PATENTS  
Washington, D.C. 20231

Sir:

Transmitted herewith for filing under 37 C.F.R. §1.53(b) is the patent application of  
Inventor(s): Satoshi ETO; Masato MATSUMIYA; Toshimi IKEDA; Yuki ISHII; Akira KIKUTAKE;  
and Kuninori KAWABATA

Case Docket No. P21-9026  
July 22, 1999

1584 U.S. PTO  
09/359767  
07/22/99

SEMICONDUCTOR MEMORY DEVICE AND METHOD FOR EXECUTING SHIFT REDUNDANCY  
OPERATION

Specification and Claims (187 pages)

87 sheets of drawings

- ☒ Newly executed Declaration and Power of Attorney  
☒ Return Receipt Postcard  
☒ An assignment of the invention to FUJITSU LIMITED with accompanying PTO-1595 Form  
☒ A filing fee, calculated as shown below:

	(Col. 1)	(Col. 2)
FOR	No. Filed	No. Extra
BASIC FEE		
TOTAL CLAIMS	98 - 20 =	* 78
INDEP CLAIMS	13 - 03 =	* 10
MULTIPLE DEPENDENT CLAIM PRESENTED		

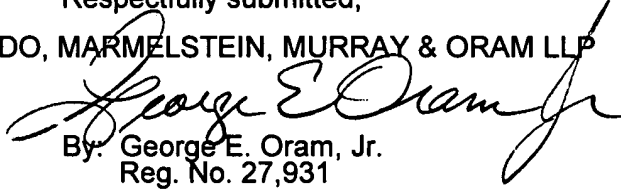
\* If the difference in Col. 1 is less than zero, enter "0" in Col. 2

Small Entity		Other Than A Small Entity	
RATE	FEE	RATE	FEE
	\$380		\$ 760
x 9 =		x 18 =	1,404
x 39 =		x 78 =	780
+130 =		+260 =	0
TOTAL			\$2,944

Check # 20268 in the amount of \$ 2,984.00 to cover the filing fee and assignment recordation fee. In the event that the attached check is found to be insufficient, the Commissioner is hereby authorized to charge payment for any additional filing fees required under 37 CFR 1.16 associated with this communication or credit any over-payment to Deposit Account No. 14-1060.

Respectfully submitted,

NIKAIDO, MARMELSTEIN, MURRAY & ORAM LLP

By:   
George E. Oram, Jr.  
Reg. No. 27,931

Metropolitan Square  
655 15th Street, N. W.  
Suite 330 - G Street Lobby  
Washington, D. C. 20005-5701  
Tel: (202) 638-5000  
Fax: (202) 638-4810

GEO:mmg